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Patent Abstracts of Japan

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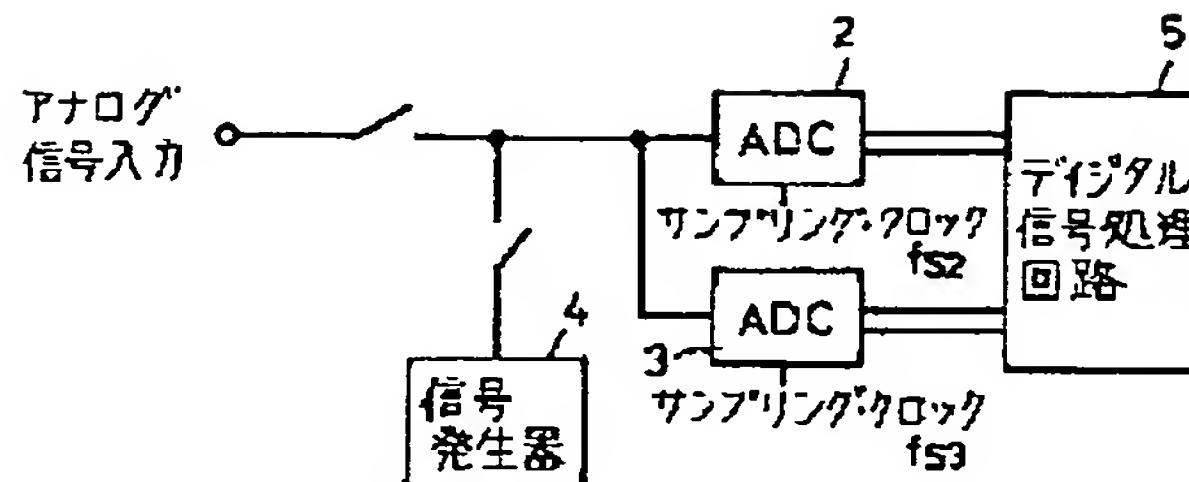
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APPLICANT : ADVANTEST CORP;

INVENTOR : SATO NORIMASA;

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TITLE : METHOD FOR CORRECTING A/D
CONVERTER ADOPTING INTERLEAVE
SYSTEM



ABSTRACT : PURPOSE: To decrease spurious waves caused by an interleave operation to be mixed into a measured result by mutually adjusting a signal frequency designated at the time of measurement and a sampling frequency.

CONSTITUTION: Designated signals in a correction program are generated by a signal generator 4 and a sampling clock generator and supplied to analog/ digital converters (ADC) 2 and 3. Then, the offset and gain between the ADC 2 and 3 are equalized. Afterwards, the signal of the generator 4 is measured by the interleave operation, and FFT analysis is performed. Thus, it is judged that the spurious is generated since a gap is not fixed at the time of alternately sampling the ADC 2 and 3. Therefore, when the frequency of the measured signal and the sampling frequency are decided, the frequency component of the spurious wave to be generated is decided, as well. Thus, the the spurious component is corrected to the minimum by performing on-line adjustment to sampling timing.

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